

IN THE CLAIMS:

Please substitute claims 1-4 with the following:

1-2. (Cancelled).

3. (Currently Amended) ~~The A~~ method for forming a capacitor ~~as claimed in claim 1,~~ comprising:

a first step for forming an amorphous silicon film so as to cover hole-type or island-type core pattern formed on a substrate,

a second step for removing said amorphous silicon film so that said amorphous silicon film remains on the side wall of said core pattern to thereby form a cylindrical bottom electrode having the peripheral wall that is said amorphous silicon film remaining on the side wall of said core pattern,

a third step for removing said core pattern by means of etching,

a fourth step for removing the natural oxide film formed on the surface of said bottom electrode and the amorphous silicon surface layer that is the component of said bottom electrode by means of etching, wherein in said fourth step dry etching is applied using CH<sub>4</sub> and O<sub>2</sub>, and

a fifth step for forming semispherical silicon grains on the surface of said bottom electrode.

4. (Cancelled).

5. (New) The method for forming a capacitor as claimed in claim 3, wherein the core pattern is formed on an etching stopper layer.

6. (New) The method for forming a capacitor as claimed in claim 5, wherein the etching stopper layer is formed on an inter-layer insulating film.